

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant	:	Verbauwhede et al.
Appl. No.	:	10/565,551
Filed	:	September 11, 2006
For	:	DYNAMIC AND DIFFERENTIAL CMOS LOGIC WITH SIGNAL- INDEPENDENT POWER CONSUMPTION TO WITHSTAND DIFFERENTIAL POWER ANALYSIS
Examiner	:	Daniel D. Chang
Group Art Unit	:	2819

**RESPONSE TO RESTRICTION REQUIREMENT DATED MAY 4, 2007**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement dated May 4, 2007 in the above-captioned application, Applicant elects Group I, Claims 1-20 for further examination.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.